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8-16-02

**PATENT**

**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

In re the application of:  
Bindiganavale S. Nataraj

Application No.: 09/815,921

Filed: March 24, 2001

Title: Method and Apparatus For Selecting a  
Most Significant Priority Number for a  
Device Using a Partitioned Priority Index  
Table

Attorney Docket No. NLM.P002

Examiner: Not Yet Assigned

Art Unit: 2661

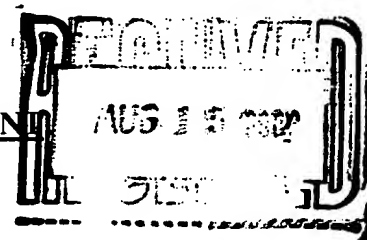
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**INFORMATION DISCLOSURE STATEMENT**



Dear Sir:

This Information Disclosure Statement is believed to be filed before the mailing date of a first Office Action on the merits under 37 CFR §1.97(b). Accordingly, it is believed that no fees are due in connection with the filing of this Information Disclosure Statement. However, if it is determined that any fees are due, the Commissioner is hereby authorized to charge such fees to Deposit Account 50-1624.

The references listed in the attached PTO Form PTO/SB/08A, copies of which are attached, may be material to examination of the above-identified patent application. Applicant submits these references in compliance with his duty of disclosure pursuant to 37 CFR §§1.56 and 1.97. The Examiner is requested to make these references of official record in this application.

**CERTIFICATE OF MAILING**

Date of Deposit August 8, 2002

I hereby certify that this correspondence is being deposited with the United States Postal Service as First Class Mail in an envelope addressed to the Assistant Commissioner for Patents, Washington, D. C. 20231 on the date indicated above.

Lawrence M. Cho

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This Information Disclosure Statement is not to be construed as a representation that a search has been made, that additional information material to the examination of this application does not exist, or that these references indeed constitute prior art.

If there are any additional charges, please charge them to Deposit Account No. 50-1624.

Respectfully submitted,

Dated: 8/8, 2002

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PTO/SB/08A (10-01)

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Substitute for form 1449A/PTO		<b>Complete if Known</b>	
		Application Number	09/815,921
<b>INFORMATION DISCLOSURE STATEMENT BY APPLICANT</b>  (use as many sheets as necessary)		Filing Date	March 24, 2001
		First Named Inventor	Bindiganavale S. Nataraj
		Art Unit	2661
		Examiner Name	Not Yet Assigned
		Attorney Docket Number	NLM.P002
Sheet	1	of	7

U.S. PATENT DOCUMENTS					
Examiner Initials <sup>1</sup>	Cite No. <sup>1</sup>	Document Number	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
		Number - Kind Code <sup>2</sup> (if known)			
		US- 3,257,646	06-21-1966	Roth	RECEIVED  AUG 14 2002  Technology Center 2600
		US- 3,353,159	11-14-1967	Lee, III	
		US- 3,602,899	08-31-1971	Lindquist et al.	
		US- 3,675,211	07-04-1972	Raviv	
		US- 3,685,020	08-15-1972	Meade	
		US- 3,868,642	02-25-1975	Sachs	
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		US- 4,472,805	09-18-1984	Wacyk et al.	
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		US- 4,758,982	07-19-1988	Price	
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		US- 4,813,002	03-14-1989	Joyce et al.	
		US- 4,845,668	07-04-1989	Sano et al.	

FOREIGN PATENT DOCUMENTS						
Examiner Initials <sup>1</sup>	Cite No. <sup>1</sup>	Foreign Patent Document	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear	T <sup>6</sup>
		Country Code <sup>3</sup> - Number <sup>4</sup> - Kind Code <sup>5</sup> (if known)				
		EP 0 267 612 A2	05-18-1988	Akita		
		EP 0 381 249 A2	08-08-1990	Akashi		
		EP 0 660 332 A1	06-28-1995	Montoye		
		EP 0 774 758 A2	05-21-1997	Rao		
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		EP 08273376 (Japan Abst)	10-18-1996	Takeshi		
		WO 98/12651	03-26-1998	Kemoke		
		WO 99/31581	06-24-1999	Therkildsen		

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		<b>Application Number</b>	09/815,921
<b>INFORMATION DISCLOSURE STATEMENT BY APPLICANT</b>  (use as many sheets as necessary)		<b>Filing Date</b>	March 24, 2001
		<b>First Named Inventor</b>	Bindiganavale S. Nataraj
		<b>Group Art Unit</b>	2661
		<b>Examiner Name</b>	Not Yet Assigned
		<b>Attorney Docket Number</b>	NLM.P002
<b>Sheet</b>	5	<b>of</b>	7

<b>OTHER PRIOR ART -- NON PATENT LITERATURE DOCUMENTS</b>			
Examiner Initials <sup>1</sup>	Cite No. <sup>1</sup>	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published	T <sup>2</sup>
		Advanced Micro Devices, "Final Am99C10A 256 x 48 Content Addressable Memory" Publication No. 08125, Rev. G, December 1992, pp. 1-21.	
		MASAO AKATA, "A Scheduling Content-Addressable Memory for ATM Space-Division Switch Control", IEEE International Solid-State Circuits Conference, Feb. 1991, New York.	
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		GEC Plessey Semiconductors, "P2800 2K x 64 Bit Multi-Port Content Addressable Memory", Preliminary Information, February 1997, pp. 1-15.	
		KANAD GHOSE, "Response Pipelined CAM Chips: The First Generation and Beyond.", 7th International Conference on VLSI Design, January 1994, pp. 365-368.	
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		SIMON R. JONES et al., "A 9-kbit Associative Memory for High Speed Parallel Processing Applications", IEEE Journal of Solid-State Circuits, Vol. 23, No. 2, April 1988, pp. 543-548.	
		HIROSHI KADOTA et al., "An 8-kbit Content-Addressable and Reentrant Memory", IEEE Journal of Solid-State Circuits, Vol. SC-20, No. 5, October 1985, pp. 951-956.	

<b>Examiner Signature</b>		<b>Date Considered</b>	
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		Group Art Unit	2661
		Examiner Name	Not Yet Assigned
		Attorney Docket Number	NLM.P002
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OTHER PRIOR ART -- NON PATENT LITERATURE DOCUMENTS			
Examiner Initials <sup>*</sup>	Cite No. <sup>1</sup>	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published	T <sup>2</sup>
		KLSI KE5B064A1 Address Processor Ver. 2.0.2 Preliminary (believed to be published late 1995 or early 1996).	
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		LANDROCK et al., "Associative Storage Module", IBM Technical Disclosure Bulletin, Vol. 25, No. 5, October 1992, pp. 2341-2342.	
		ANTHONY MCAULEY et al., "A Self-Testing Reconfigurable CAM", IEEE Journal of Solid-State Circuits, Vol. 26, No. 3, March 1991, pp. 257-261.	
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		MUSIC Semiconductors, "MU9C1480 LANCAM" Advance Information, March 22, 1990, pp. 1-11.	
		MUSIC Semiconductors, "MU9C1480 LANCAM Handbook", November 1994.	
		MUSIC Semiconductors, "MU9C2480 LANCAM" Preliminary Data Sheet, August 25, 1995, pp. 1-24.	
		MUSIC Semiconductors, "MUAA CAM Family" Advanced Information, February 24, 1998, Rev. 0, pp. 1-16.	

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		TAKESHI OGURA et al., "A 4-kbit Associative Memory LSI", IEEE Journal of Solid-State Circuit, Vol. SC-20, No. 6, December 1985, pp. 1277-1281.	
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		KEIKICHI TAMARU, "The Trend of Functional Memory Development", INVITED PAPER Special Issue on LSI Memories, IEICE Trans. Electron, Vol. E76-C, No. 11, Nov. 1993, pp. 1545-1554.	
		XIPENG XIAO and LIONEL M. NI, "Internet QoS: A Big Picture", IEEE Network, March/April 1999, pp. 8-18.	
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